IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Charles E. Dumont   :   Date: August 6, 2003

Serial Number: Concurrent   :   Group Art Unit: Unknown

Filed: Concurrent   :   Examiner: Unknown

Title: METHOD, SYSTEM AND   :   IBM CORPORATION
PROGRAM PRODUCT FOR   John R. Pivnichny, Ph.D.
VALIDATING REMOTELY CACHED   :   IPLaw IQ0A/040-3
DYNAMIC CONTENT WEB PAGES   1701 North Street
                                             :   Endicott, NY 13760

INFORMATION DISCLOSURE STATEMENT (IDS) UNDER 37 CFR 1.97

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The attached Form PTO-1449, and any attached foreign patent office search report, provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that the information provided is prior art.

This IDS is submitted under:

__X__ 37 CFR 1.97(b) - No Fee.

_____ 37 CFR 1.97(c) - No Fee. Each item of information contained in this information disclo- .
sure statement was cited in a communication from a foreign patent office in a counterpart foreign application. This communication is dated _____, which is not more than 3 months prior to the mailing of this statement.

_____ 37 CFR 1.97(c) - Fee.

_____ 37 CFR 1.97(d) - Fee, Certification & Petition.

The Commissioner is authorized to charge payments of any associated fees required under 37 CFR 1.17(p) and (i) (1) to Deposit Account No. 122158 (IBM).

Respectfully submitted,

By: _____

Ronald A. D'Alessandro
Registration No. 42,456
Telephone No.: (518) 449-0044

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Applicant(s): Charles E. Dumont

FILING	GROUP

U.S. PATENT DOCUMENTS

MINER AL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		US 2002/0194382 A1	12/2002	Kausik et al.			
		5,550,995	08/1996	Barrera et al.			
		5,787,479	07/1998	Jessani et al.			
		6,157,986	12/2000	Witt			
		US 6,175,906 B1	01/2001	Christie			
		US 6,351,767 B1	02/2002	Batchelder et al.			
		US 6,408,360 B1	06/2002	Chamberlain et al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
		WO 02/099660 A1	12/2002	PCT				

OTHER DOCUMENTS   *(Including Author, Title, Date, Pertinent Pages, Etc.)*

			IBM Technical Disclosure Bulletin, "Sub-Block Cache with Demand Sub-Block Reloading in Conjunction with A Risc System", Vol. 35, No. 1A, June 1992, pp. 84-85.
			INSPEC, Yonghwan, L. et al., 1997-1998.

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER DOCUMENTS   *(Including Author, Title, Date, Pertinent Pages, Etc.)*

			International Semiconductor Conference, 20th Edition, October 7-11, 1997, Sinaia, Romania, CAS '97 Proceedings, Vol. 1, Lee, Y. et al. "Shared Tag for MMU and Cache Memory", pp. 77-80.
			Electronics Letters, Vol. 33, Issue 21, October 9, 1997, Lee, Y. et al., "Indirectly-Compared Cache Tag Memory Using a Shared Tag in a TLB", pp. 1764-1766.

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		Barrera, "Sub-Block Cache with Demand Sub-Block Reloading in Conjunction with a RISC System", Technical Disclosure Bulletin, No. 1A, June 1992, pp. 84-85.
		Jeffrey C. Mogul, "Clarifying the Fundamentals of HTTP", May 2002, pp. 25-36.

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			Kelly et al., "Aliasing on the World Wide Web: Prevalence and Performance Implications", May 2002, pp. 281-292.
			IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Lee, Y. et al., "Unified Tag Memory Architecture with Snoop Support", Vol. E81-A, Issue 6, June 1998, pp. 1172-75.

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